

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1159	"702"/\$.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:09
L2	141	"702"/89,186,187.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:10
L3	35	"702"/89,186,187.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:10
L4	33	"702"/89,186,187.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:10
L5	17	"702"/89,186,187.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5 and counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:11
L6	0	"702"/89,186,187.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5 and counter and accumulator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:16
L7	0	717/118,128,130,131.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5 and counter and accumulator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:15
L8	0	714/25,35,38,45,48.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5 and counter and accumulator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:16
L9	0	257/49,51,603.ccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5 and counter and accumulator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:16

L10	1	"702"/\$.cccls. and (performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and thread and load\$5 and counter and accumulator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:16
S1	17	"6016466"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 16:16
S2	45218	"6016466"and counter\$3 and load\$4 and updat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/05 14:16
S3	0	"6016466".pn. and counter\$3 and load\$4 and updat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/05 14:17
S4	7	"6016466" and counter\$3 and load\$4 and updat\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/05 14:17
S5	7	"6016466" and counter\$3 and load\$4 and updat\$4 and perform\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/05 14:18
S6	23530	direct near3 memory near4 access	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/05 14:19
S7	6	"5423029" and direct near3 memory near4 access and (processor cpu controller microporocessor microcontroller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/05 14:21
S8	21029	(performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/04 16:08
S9	5746	(performance near7 monitor\$4) and (comput\$5 processor microprocessor microcomputer controller) and counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:37

S10	4506	S9 and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:38
S11	4506	S10 and performance	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:39
S12	3074	S10 and performance and bit\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:39
S13	862	S10 and performance and bit\$3 and (least most) near4 significant\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:40
S14	188	S10 and performance and bit\$3 and (least most) near4 significant\$3 and thread	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:49
S15	23	S10 and performance and bit\$3 and (least most) near4 significant\$3 and thread near5 switch\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 14:15
S16	0	S14 and "6016466"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 13:50
S17	56651	international adj3 business adj2 machine	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 14:16
S18	0	aus920030615us1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/01 16:17